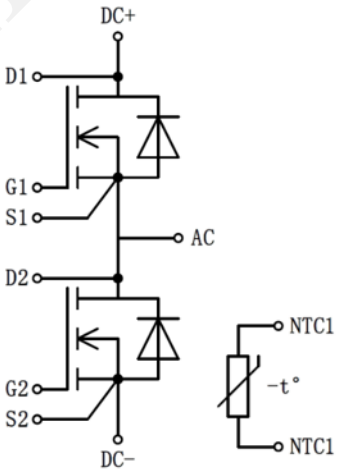
SiC Half-Bridge Module
PAAA07036FM

Features

- High Blocking Voltage with Low $R_{DS(on)}$
- High Temperature Operation
- Low Stray Inductance

Applications

- Automotive Application
- EV/HEV



Standards Benefits

- Improve System Efficiency
- Improve Power Density
- Reduce System Size

Order Information

Part Number	Package	Marking
PAAA07036FM	IPM-MSOP9	PAAA07036FM

Contents

1. Maximum Ratings

At T_J= 25℃, unless specified otherwise

Parameter	Symbol	Value	Unit	Test Conditions
Drain - Source Voltage	V_{DSmax}	750	V	
Gate - Source Voltage (Dynamic)	V_{GSmax}	-10 / +22	V	AC (f >1Hz)
Gate - Source Voltage(static) turn-on gate voltage turn-off gate voltage	 $V_{GS,on}$ $V_{GS,off}$	 +15/+18 -3	V	Static
Continuous Drain Current	I_D	36	A	V_{GS} =18V Tc = 25℃ T_J = 175℃
		26		V_{GS} =18V Tc = 100℃ T_J = 175℃
Power Dissipation	P_D	180	W	
Operating Junction Temperature	T_J	-55 To +175	℃	
Storage Temperature	T_{stg}	-55 To +175	℃	

2. Electrical Characteristics

At T_J= 25℃, unless specified otherwise

Parameter	Symbol	Value			Unit	Test Conditions
		Min.	Typ.	Max.		
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	750	/	/	V	V_{GS}=0V
Gate Threshold Voltage	$V_{GS(th)}$	/	3.1	/	V	V_{DS}= V_{GS} I_D= 5.5mA
Reverse Bias Drain Current	I_{DSS}	/	1.8	100	μA	V_{GS}= 0V V_{DS}= 750V
Gate-Source Leakage Current	I_{GSS}	/	20	250	nA	V_{GS}= 18V V_{DS}= 0V
Drain-Source On-State Resistance	$R_{DS(on)}$	/	55	70	mΩ	V_{GS}= 18V I_D= 20A T_J= 25℃
		/	58	/		V_{GS}= 18V I_D= 20A T_J= 175℃
		/	70	/		V_{GS}= 18V I_D= 20A T_J= 25℃
Internal Gate Resistance	$R_{G(int)}$	/	1.2	/	Ω	f= 1MHz V_{AC}= 25mV
Coss Stored Energy	E_{oss}	/	14	/	μJ	V_{DS}= 400V f= 1MHz
Turn-on Energy	E_{on}	/	109	/	μJ	V_{DS}= 400V V_{GS}= -3/18V I_D= 20A R_G= 1Ω
Turn-off Energy	E_{off}	/	25	/		
Input Capacitance	C_{iss}	/	1853	/	pF	V_{GS}= 0V V_{DS}= 400V f= 1MHz V_{AC}= 25mV
Output Capacitance	C_{oss}	/	141	/	pF	

Parameter	Symbol	Value			Unit	Test Conditions
		Min.	Typ.	Max.		
Reverse Transfer Capacitance	C_{rss}	/	8.3	/	pF	
Gate to Source Charge	Q_{gs}	/	17.5	/	nC	V_{DS}= 400V I_{DS}= 20A V_{GS}= -3 to 18V
Gate to Drain Charge	Q_{gd}	/	15.3	/		
Total Gate Charge	Q_g	/	53.1	/		
Turn-On Delay Time	$t_{d(on)}$	/	16	/	ns	V_{DS}= 400V V_{GS}= -3/18V I_D= 20A R_G= 1Ω
Rise Time	t_r	/	20.1	/		
Turn-Off Delay Time	$t_{d(off)}$	/	22	/		
Fall Time	t_f	/	16	/		

3. Reverse Diode Characteristics

At T_J= 25℃, unless specified otherwise

Parameter	Symbol	Value		Unit	Test Conditions
		Typ.	Max.		
Diode Forward Voltage	V_{SD}	5.2	/	V	V_{GS}= -3V I_{SD}= 20A T_J= 25℃
		4.8	/	V	V_{GS}= -3V I_{SD}= 20A T_J= 175℃
Continuous Diode Forward Current	Is	29	/	A	V_{GS}=-3V
Peak Reverse Recovery Current	I_{rrm}	39	/	A	V_{GS}= -3V I_{SD}= 20A V_R= 800V d_{if}/d_t= 4100A/μs T_J= 25℃

4. Thermal Characteristics

Parameter	Symbol	Value			Unit	Test Conditions
		Min.	Typ.	Max.		
Thermal Resistance from Junction to Cooling Fluid	$R_{\theta JF}$	/	0.92	/	℃/W	

5. NTC Thermistor Characteristics

Parameter	Symbol	Value			Unit	Test Conditions
		Min.	Typ	Max.		
Rated Resistance	R_{NTC}	/	10	/	kΩ	T_{NTC}= 25℃
Resistance Tolerance	△R/R	-5	/	5	%	T_{NTC}= 25℃
B-value	$B_{25/50}$	/	3380	/	K	T_2= 50℃
B-value	$B_{25/85}$	/	3435	/	K	T_2= 80℃
B-value	$B_{25/100}$	/	3485	/	K	T_2= 100℃
Power Dissipation	P_{NTC}	/	/	10	mW	T_{NTC}= 25℃

6. Typical Performance

At T_J= 25℃, unless specified otherwise

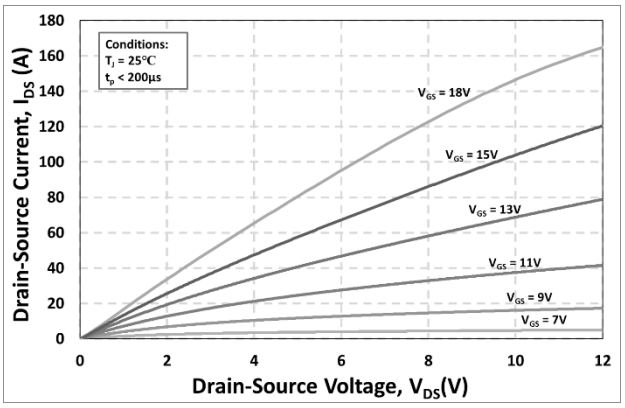


Figure 1. Output Characteristics T_J = 25℃

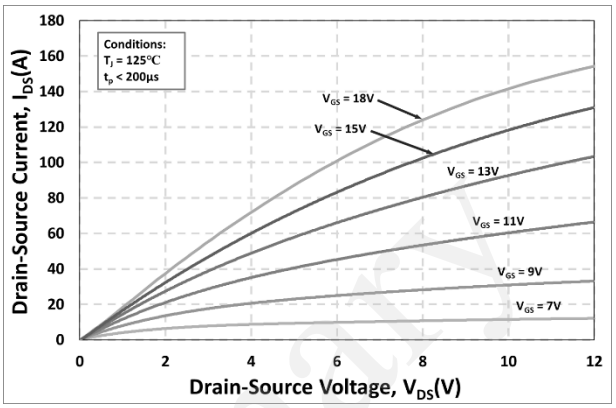


Figure 2. Output Characteristics T_J = 125℃

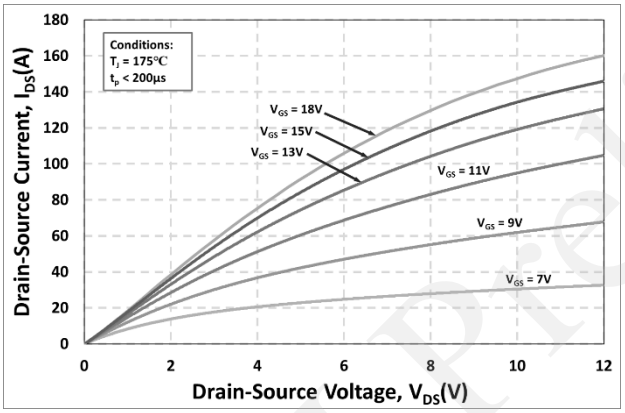


Figure 3. Output Characteristics T_J = 175℃

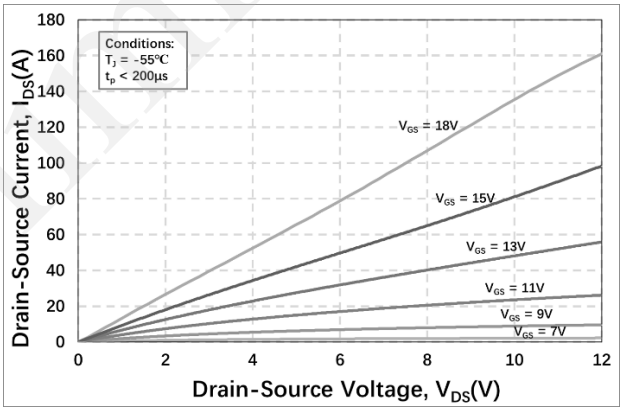


Figure 4. Output Characteristics T_J = -55℃

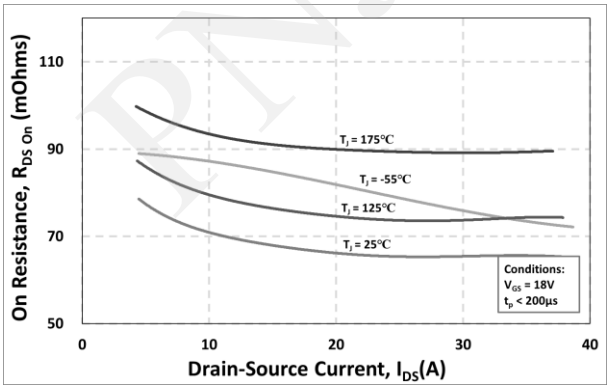


Figure 5. On-Resistance vs. Drain Current Various Temperatures

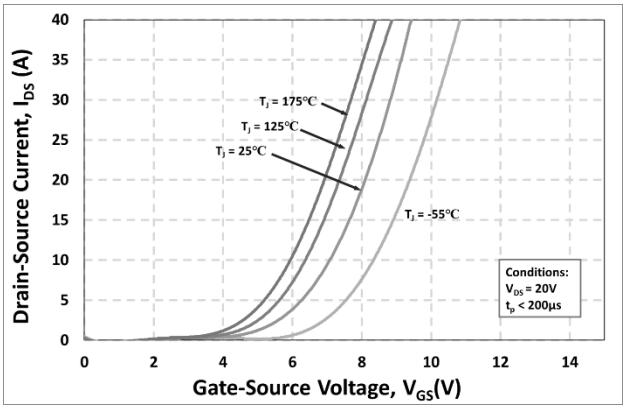


Figure 6. Transfer Characteristic for Various Junction Temperatures

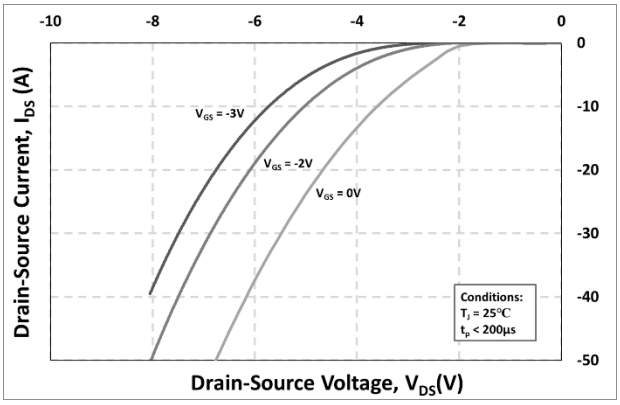


Figure 7. Body Diode Characteristic at 25°C

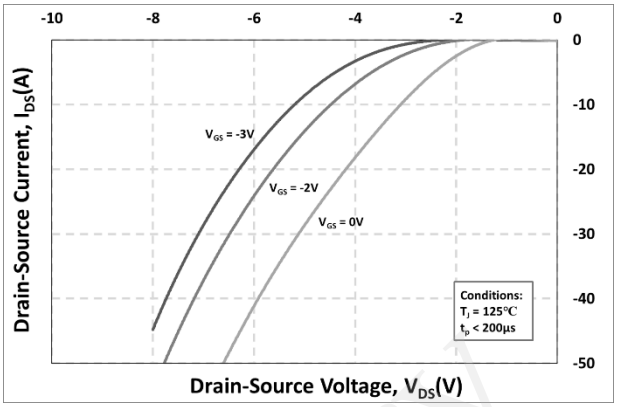


Figure 8. Body Diode Characteristic at 125°C

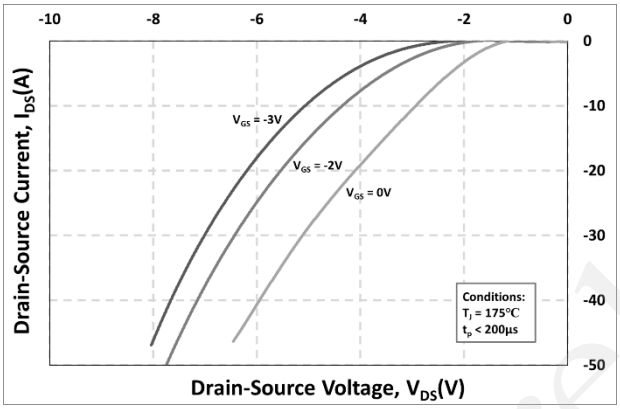


Figure 9. Body Diode Characteristic at 175°C

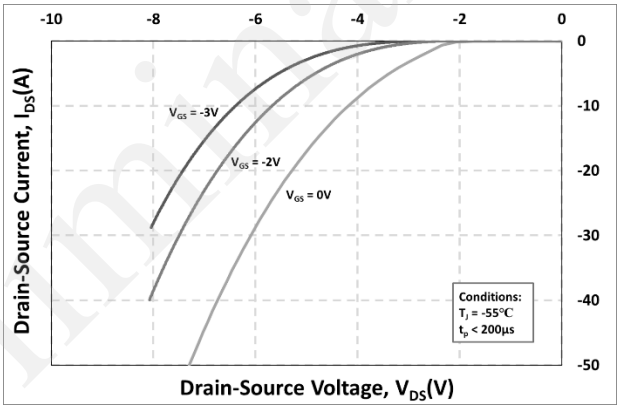


Figure 10. Body Diode Characteristic at -55°C

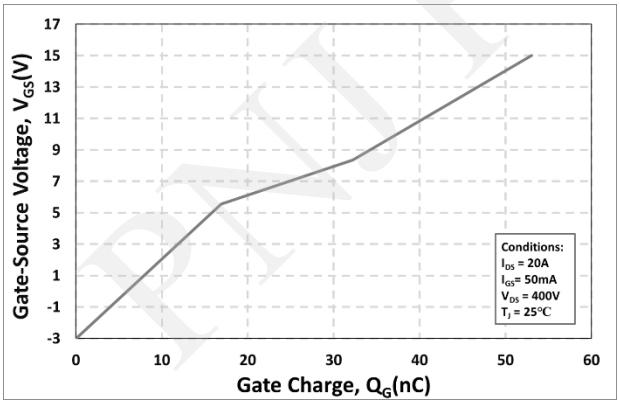


Figure 11. Gate Charge Characteristics

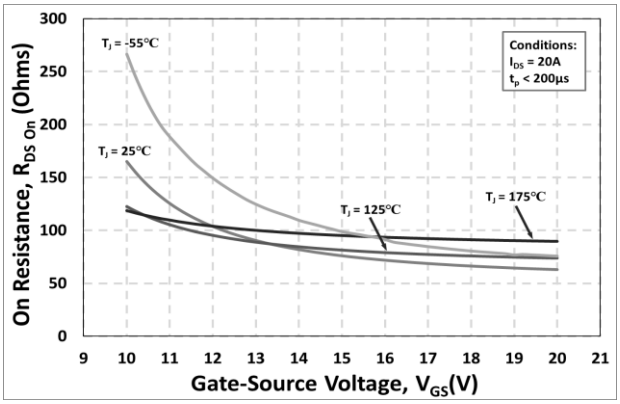


Figure 12. On-Resistance vs. Gate-Source Voltage

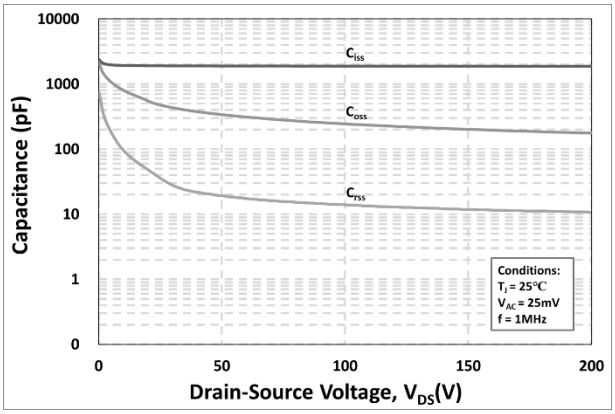


Figure 13. Capacitances vs. Drain-Source Voltage (0 - 200V)

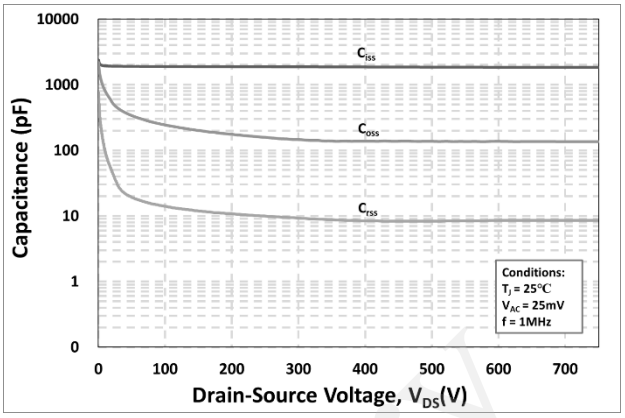


Figure 14. Capacitances vs. Drain-Source Voltage (0 - 750V)

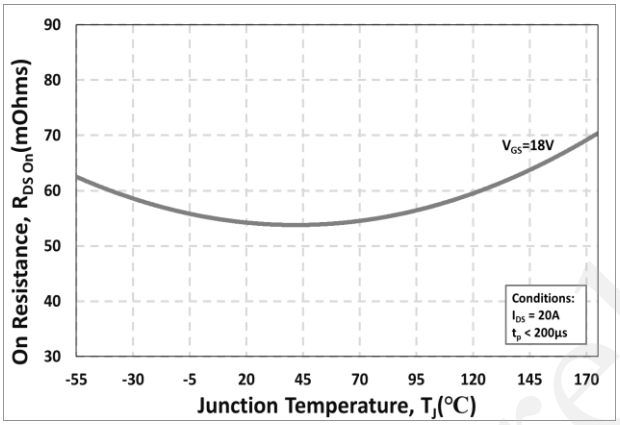


Figure 15. On-Resistance vs. Temperature

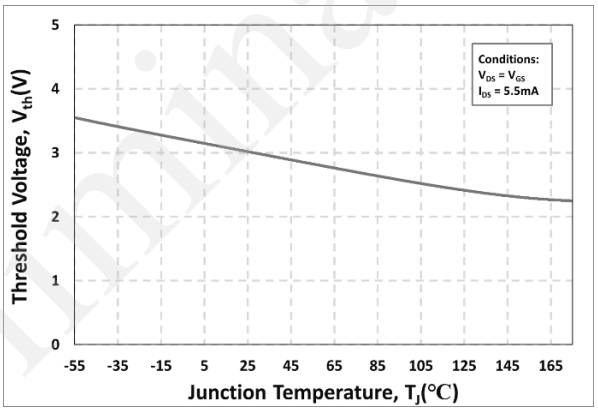
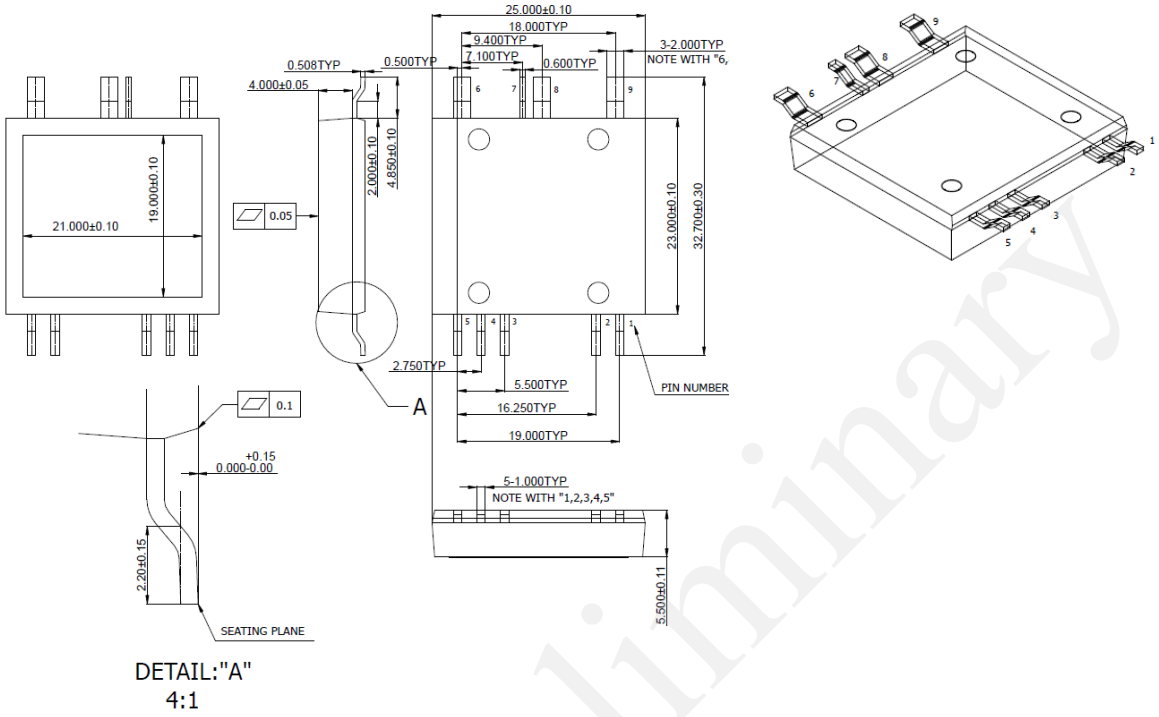


Figure 16. Threshold Voltage vs. Temperature

7. Package Outlines



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