

SiC Half-Bridge Module PAIA12050AM

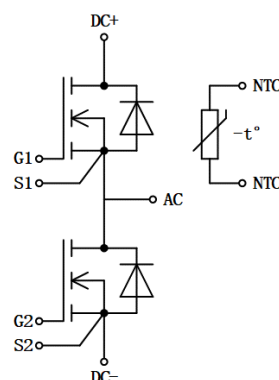
Features

- High blocking voltage
- Low on-state resistance
- Low stray inductance
- Low switching loss
- Low thermal resistance



Applications

- High-frequency switching application
- DC/DC converter
- Motor drives
- Solar inverter
- Electric vehicle
- UPS systems



Standards Benefits

- Improve system efficiency
- Improve power density
- Reduce cooling requirement
- Reduce system cost

Order Information

Part Number	Package	Marking
PAIA1250AM	EASY 1B	PAIA1250AM

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1. Maximum Ratings

At $T_J = 25^\circ\text{C}$, unless specified otherwise

Parameter	Symbol	Value	Unit	Test Conditions
Drain - Source Voltage	V_{DSmax}	1200	V	$V_{GS}=0V$ $I_D = 100\mu A$
Gate - Source Voltage (Dynamic)	V_{GSmax}	-8 / +21	V	AC (f > 1Hz)
Gate - Source Voltage(static) turn-on gate voltage turn-off gate voltage	$V_{GS,on}$ $V_{GS,off}$	+15 / +18 -3	V	Static
Continuous Drain Current	I_D	80	A	$V_{GS} = 15V$ $T_C = 25^\circ\text{C}$ $T_J = 150^\circ\text{C}$
		50		$V_{GS} = 15V$ $T_C = 100^\circ\text{C}$ $T_J = 150^\circ\text{C}$
Operating Junction Temperature	T_J	-40 To +150	$^\circ\text{C}$	

2. Electrical Characteristics

At $T_J = 25^\circ\text{C}$, unless specified otherwise (Per Position)

Parameter	Symbol	Value			Unit	Test Conditions
		Min.	Typ.	Max.		
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	1200	/	/	V	$V_{GS}=0V$
Gate Threshold Voltage	$V_{GS(th)}$	1.8	2.7	/	V	$V_{DS} = V_{GS}$ $I_D = 17.7mA$ $T_J = 25^\circ\text{C}$
Reverse Bias Drain Current	I_{DSS}	/	1	100	μA	$V_{GS} = 0V$ $V_{DS} = 1200V$

Parameter	Symbol	Value			Unit	Test Conditions
		Min.	Typ.	Max.		
Gate-Source Leakage Current	I _{GSS}	/	20	250	nA	V _{GS} = 15V V _{DS} = 0V
Drain-Source On-State Resistance	R _{DS(on)}	/	25	35	mΩ	V _{GS} = 15V I _D = 50A
Transconductance	g _{fs}	/	28	/	S	V _{DS} = 20V I _{DS} = 50A T _J = 25°C
Turn-on Energy	E _{on}	/	0.31	/	mJ	V _{DS} = 600V I _{DS} = 50A V _{GS} = -3V/15V R _G = 1.0Ω L= 100μH
Turn-off Energy	E _{off}	/	0.18	/		
Turn-On Delay Time	T _{d(on)}	/	18.2	/	nS	
Rise Time	T _r	/	26.7	/		
Turn-Off Delay Time	T _{d(off)}	/	40.0	/		
Fall Time	T _f	/	12.8	/		
Internal Gate Resistance	R _{G(int)}	/	3.0	/	Ω	f= 100kHz V _{AC} = 25mV
Input Capacitance	C _{iss}	/	2.0	/	nF	V _{GS} = 0V V _{DS} = 800V f= 100kHz V _{AC} = 25mV
Output Capacitance	C _{oss}	/	0.07	/	nF	
Reverse Transfer Capacitance	C _{rss}	/	6.4	/	pF	
Thermal Resistance from Junction to Case	R _{θJC}	/	0.40	/	°C/W	

3. Reverse Diode Characteristics

At $T_J = 25^\circ\text{C}$, unless specified otherwise (Per Position)

Parameter	Symbol	Value		Unit	Test Conditions
		Typ.	Max.		
Diode Forward Voltage	V_{SD}	5.2	/	V	$V_{GS} = -3\text{V}$ $I_{SD} = 25\text{A}$ $T_J = 25^\circ\text{C}$
		4.6	/	V	$V_{GS} = -3\text{V}$ $I_{SD} = 25\text{A}$ $T_J = 150^\circ\text{C}$
Continuous Diode Forward Current	I_S	50	/	A	$V_{GS} = -3\text{V}$
Reverse Recover Time	t_{rr}	13.4	/	ns	$V_{GS} = -3/15\text{V}$ $I_{SD} = 50\text{A}$ $V_R = 600\text{V}$ $d_{if}/d_t = 5300\text{A}/\mu\text{s}$ $R_G = 1.0\Omega$ $T_J = 25^\circ\text{C}$
Reverse Recovery Charge	Q_{rr}	0.55	/	μC	
Peak Reverse Recovery Current	I_{rrm}	74.6	/	A	
Reverse Recovery Energy	E_{RR}	0.27	/	mJ	$V_R = 600\text{V}$ $I_{SD} = 50\text{A}$ $V_{GS} = -3\text{V}/15\text{V}$ $R_G = 1.0\Omega$ $L = 100\mu\text{H}$

4. Module Physical Characteristics

Parameter	Symbol	Value			Unit	Test Conditions
		Min.	Typ.	Max.		
Case Temperature	T_C	-40	/	125	$^\circ\text{C}$	

Parameter	Symbol	Value			Unit	Test Conditions
		Min.	Typ.	Max.		
Weight	W	/	23	/	g	
Mounting Force Per Clamp	F	20	/	50	N	
Case Isolation Voltage	V _{isol}	/	3	/	kV	AC, 50Hz, 1min
Comparative Tracking Index	CTI	200	/	/		

5. NTC Thermistor Characteristics

Parameter	Symbol	Value			Unit	Test Conditions
		Min.	Typ.	Max.		
Rated Resistance	R _{NTC}	/	5	/	kΩ	T _{NTC} = 25°C
Resistance Tolerance	ΔR/R	-5	/	5	%	T _{NTC} = 25°C
B-value	B _{25/50}	/	3380	/	K	T ₂ = 50°C
B-value	B _{25/85}	/	3435	/	K	T ₂ = 80°C
B-value	B _{25/100}	/	3485	/	K	T ₂ = 100°C
Power Dissipation	P _{NTC}	/	/	10	mW	T _{NTC} = 25°C

6. Typical Performance

At $T_J = 25^\circ\text{C}$, unless specified otherwise (Per Position)

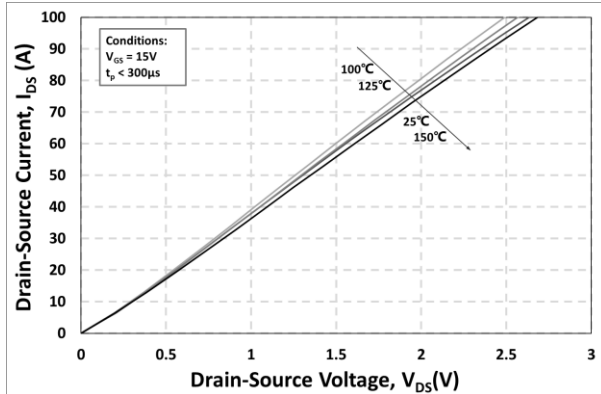


Figure 1. Output Characteristics for Various Junction Temperatures

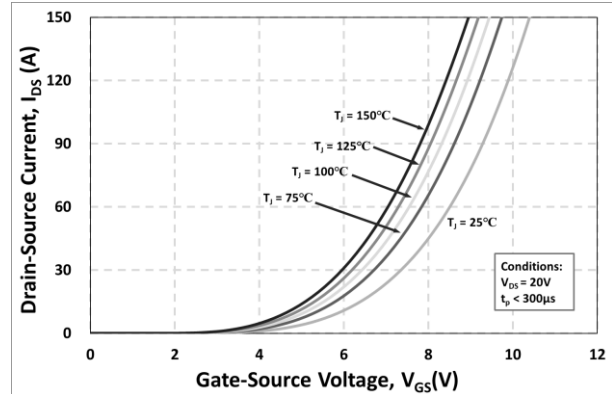


Figure 2. Transfer Characteristic for Various Junction Temperatures - State Resistance vs. Junction Temperature

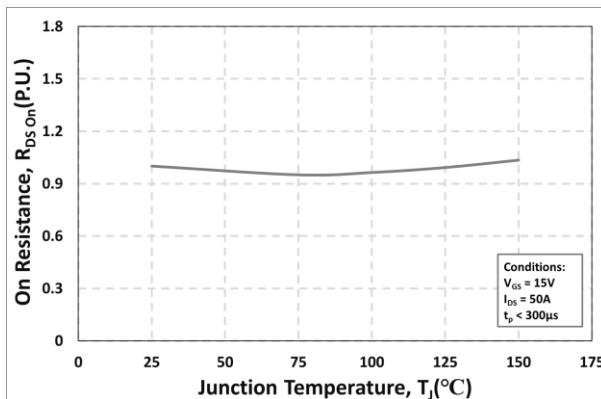


Figure 3. Normalized On-State Resistance vs. Junction Temperature

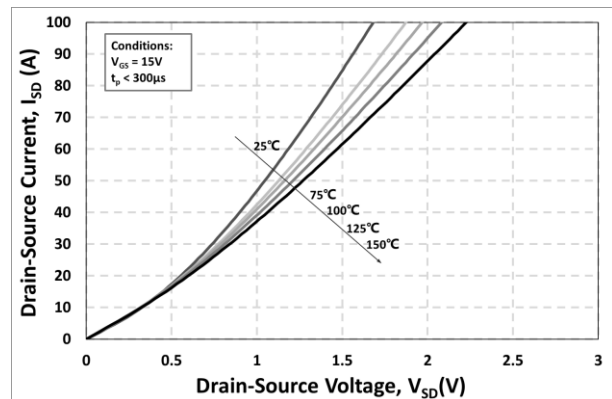


Figure 4. 3rd Quadrant Characteristic vs. Junction Temperatures at $V_{GS} = 15\text{V}$

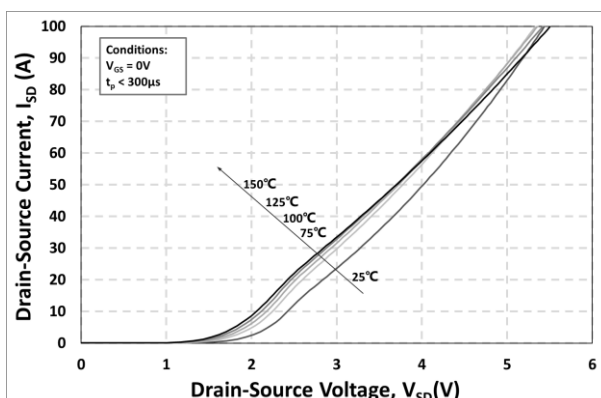


Figure 5. 3rd Quadrant Characteristic vs. Junction Temperatures at $V_{GS} = 0\text{V}$ (Body Diode)

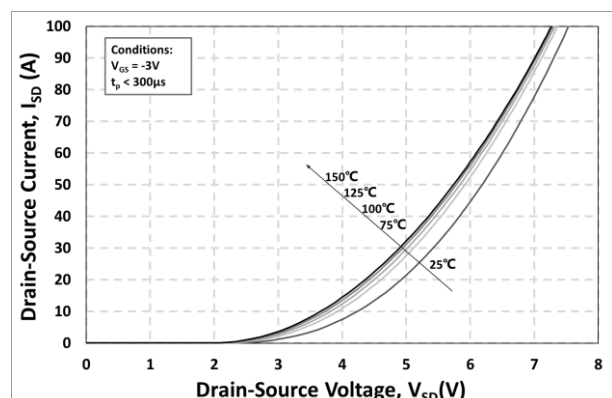


Figure 6. 3rd Quadrant Characteristic vs. Junction Temperatures at $V_{GS} = -3\text{V}$ (Body Diode)

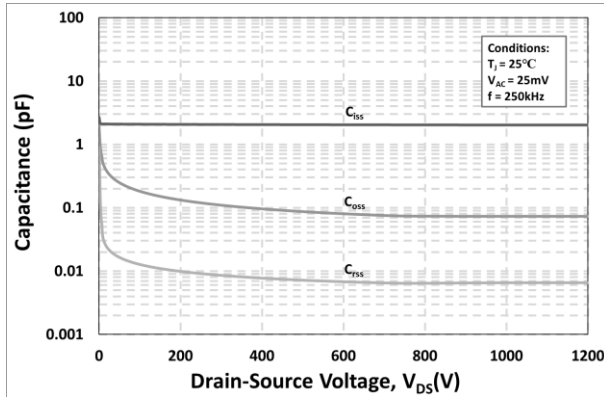


Figure 7. Capacitances vs. Drain-Source Voltage (0 - 1200V)

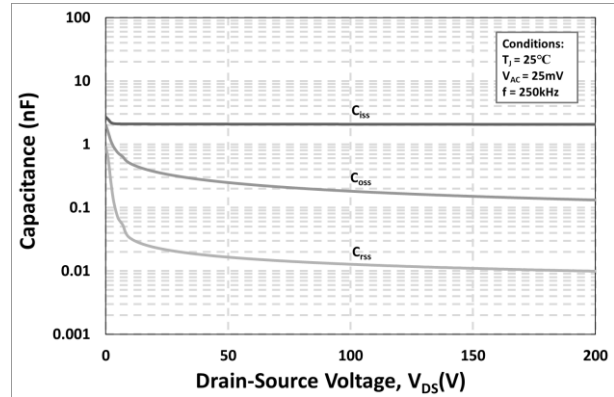


Figure 8. Capacitances vs. Drain-Source Voltage (0 - 200V)

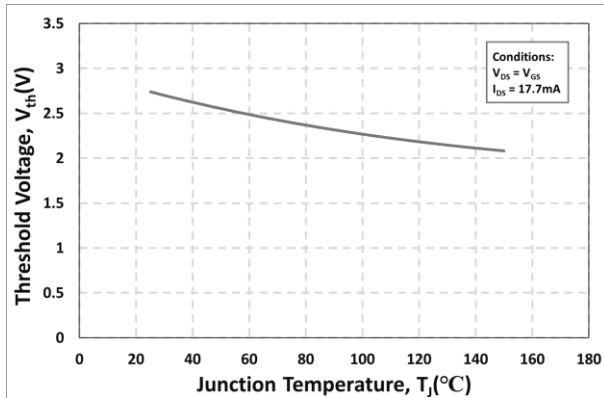


Figure 9. Threshold Voltage vs. Junction Temperature

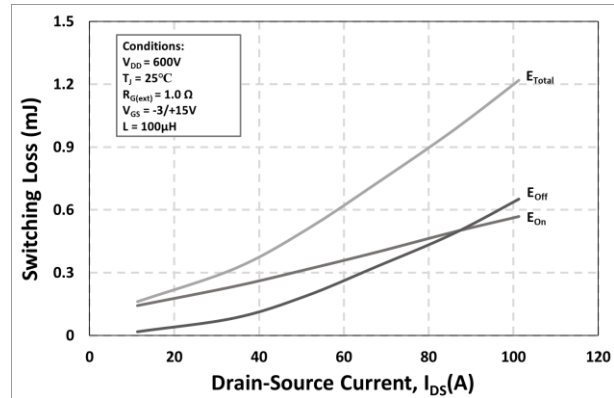


Figure 10. Switching Loss vs. Drain-Source Current ($V_{DS} = 600V$)

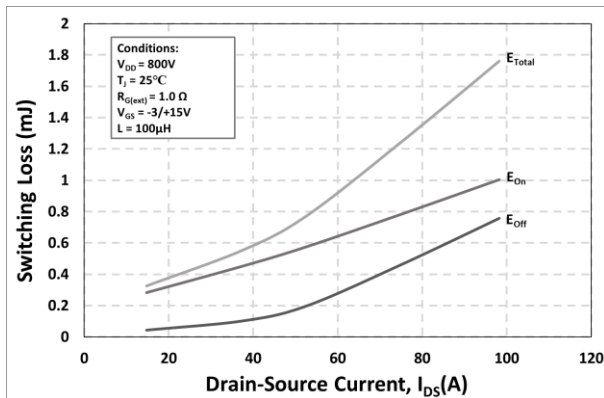


Figure 11. Switching Loss vs. Drain-Source Current ($V_{DS} = 800V$)

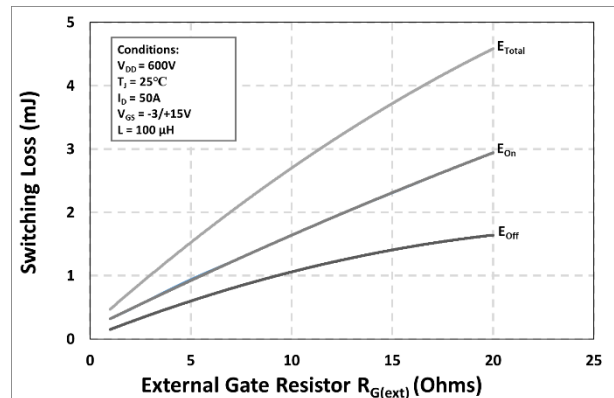


Figure 12. Switching Loss vs. External Gate Resistance

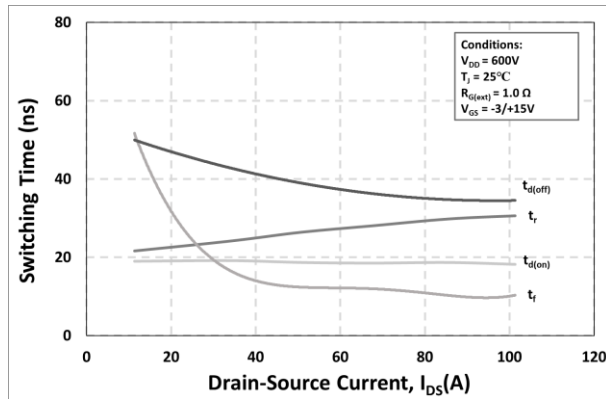


Figure 13. Switching Times vs. Drain-Source Current

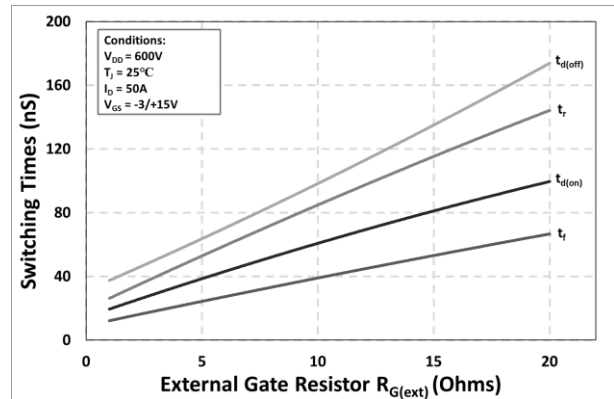


Figure 14. Switching Times vs. External Gate Resistance

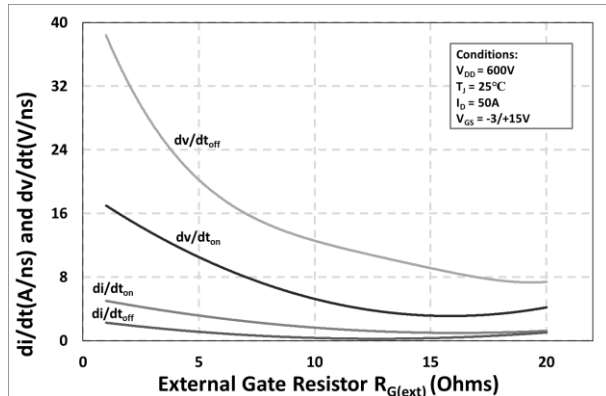


Figure 15. dv/dt and di/dt vs. External Gate Resistance

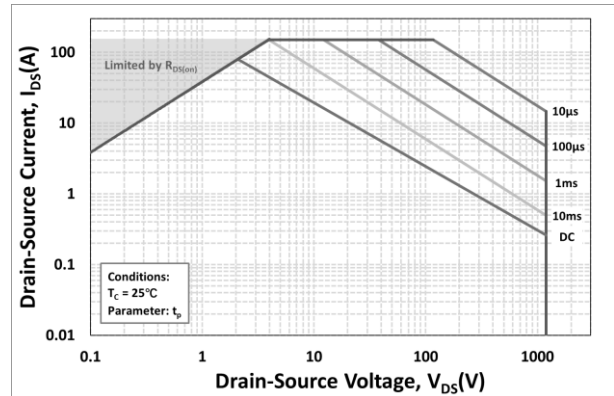


Figure 16. Forward Bias Safe Operating Area (FBSOA)

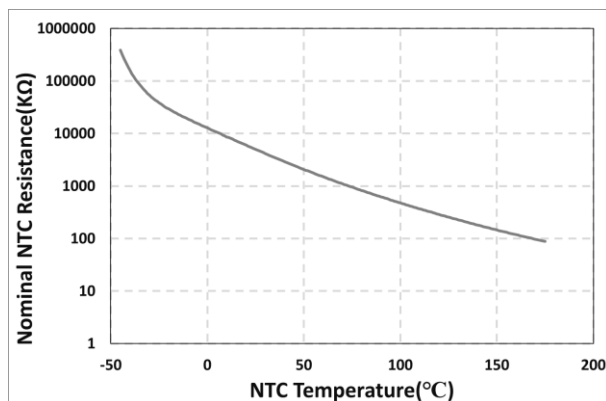


Figure 17. Nominal NTC Resistance vs. NTC Temperature

7. Definitions

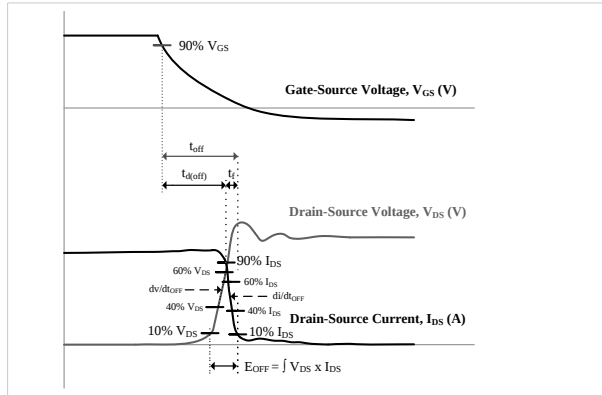


Figure 18. Turn-off Transient Definitions

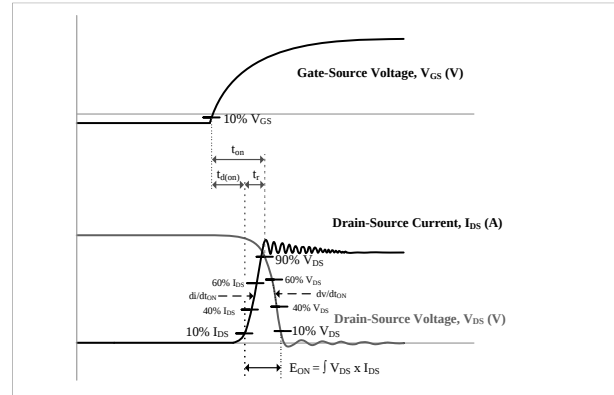


Figure 19. Turn-on Transient Definitions

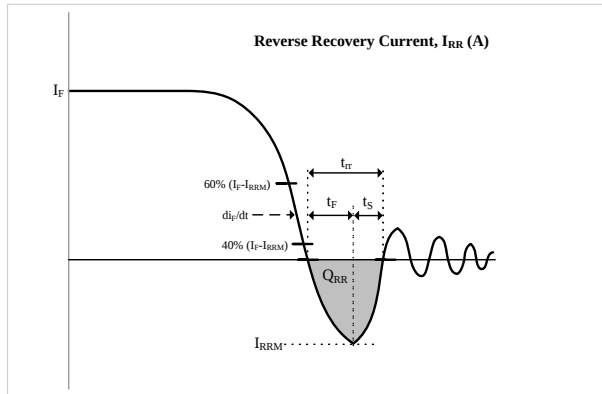


Figure 20. Reverse Recovery Definitions

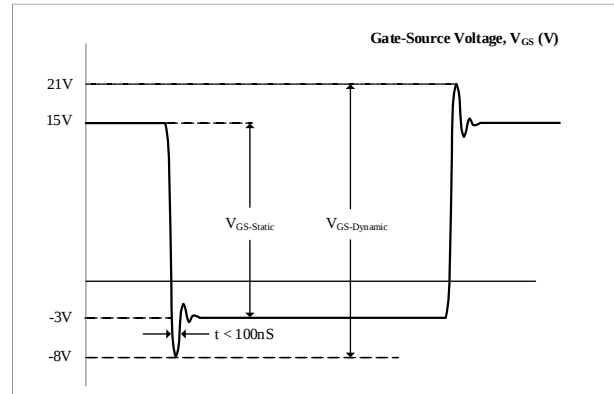
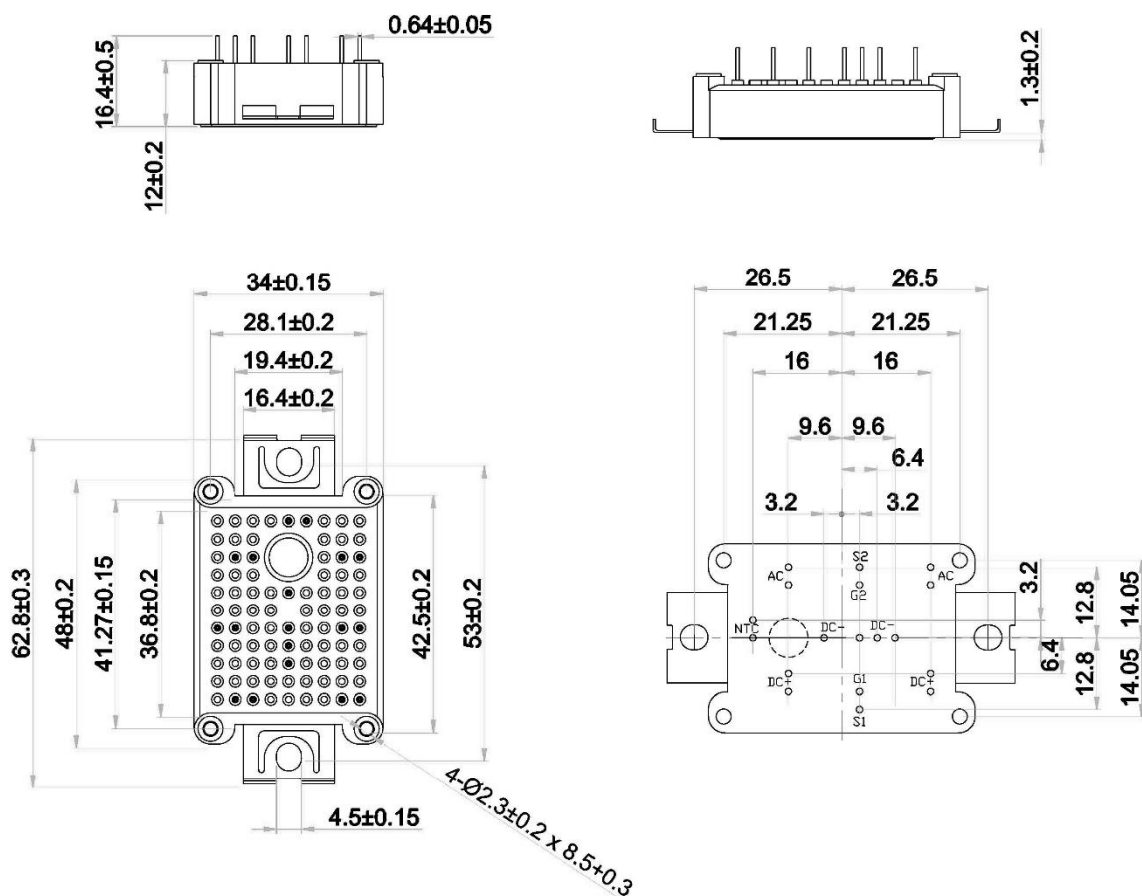


Figure 21. v_{GS} Transient Definitions

8. Package Outlines



Drawing and Dimensions

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