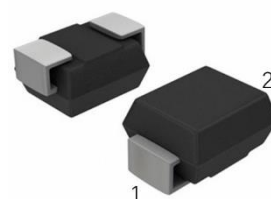


SiC SBD P3D06002B0

650V SiC Schottky Diode

Features

- Ultra-Fast Switching
- Zero Reverse Recovery Current
- High-Frequency Operation
- Positive Temperature Coefficient on V_F
- High Surge Current
- 100% UIS Tested



SMB

Cathode	1
Anode	2



Standards Benefits

- Improve System Efficiency
- Reduction of Heat Sink Requirement
- Essentially No Switching Losses
- Parallel Devices Without Thermal Runaway



Applications

- Consumer SMPS
- Boost Diodes in PFC or DC/DC Stages
- AC/DC Converters



Order Information

Part Number	Package	Marking
P3D06002B0	SMB	P3D06002B0



Contents

Features.....	1
Standards Benefits	1
Applications.....	1
Order Information	1
Contents.....	2
1. Maximum Ratings.....	3
2. Thermal Characteristics	3
3. Electrical Characteristics	4
4. Typical Performance	5
5. Package Outlines	6

PNJ Preliminary

1. Maximum Ratings

At $T_J = 25^\circ\text{C}$, unless specified otherwise

Parameter	Symbol	Value	Unit	Test condition
Repetitive Peak Reverse Voltage	V_{RRM}	650	V	$T_C = 25^\circ\text{C}$
Surge Peak Reverse Voltage	V_{RSM}	650	V	$T_C = 25^\circ\text{C}$
DC Blocking Voltage	V_R	650	V	$T_C = 25^\circ\text{C}$
Forward Current	I_F	5 3 2	A	$T_C = 25^\circ\text{C}$ $T_C = 125^\circ\text{C}$ $T_C = 145^\circ\text{C}$
Repetitive Peak Forward Surge Current	I_{FRM}	9 5	A	$T_C = 25^\circ\text{C}, t_p = 10\text{ms}$ $T_C = 125^\circ\text{C}, t_p = 10\text{ms}$
Power Dissipation	P_{tot}	19	W	$T_C = 25^\circ\text{C}$
Operating Junction and Storage Temperature	T_J, T_{STG}	-55 to +175	$^\circ\text{C}$	

2. Thermal Characteristics

Parameter	Symbol	Values	Unit
Thermal Resistance from Junction to Case	$R_{\theta JC}$	7.72	$^\circ\text{C/W}$

3. Electrical Characteristics

At $T_J = 25^\circ\text{C}$, unless specified otherwise

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Forward Voltage	V_F	/	1.5	1.7	V	$I_F = 2\text{A}$, $T_J = 25^\circ\text{C}$
			2.0	/		$I_F = 2\text{A}$, $T_J = 175^\circ\text{C}$
Reverse Current	I_R	/	1.5	10	μA	$V_R = 650\text{V}$, $T_J = 25^\circ\text{C}$
			104	/		$V_R = 650\text{V}$, $T_J = 175^\circ\text{C}$
Total Capacitance	C	/	63.3	/	pF	$V_R = 0\text{V}$, $T_J = 25^\circ\text{C}$ $f = 1\text{MHz}$
			5.61			$V_R = 200\text{V}$, $T_J = 25^\circ\text{C}$ $f = 1\text{MHz}$
			4.23			$V_R = 400\text{V}$, $T_J = 25^\circ\text{C}$ $f = 1\text{MHz}$
Total Capacitive Charge	Q_C	/	3.02	/	nC	$V_R = 400\text{V}$, $I_F = 2\text{A}$ $T_J = 25^\circ\text{C}$

4. Typical Performance

At $T_J = 25^\circ\text{C}$, unless specified otherwise

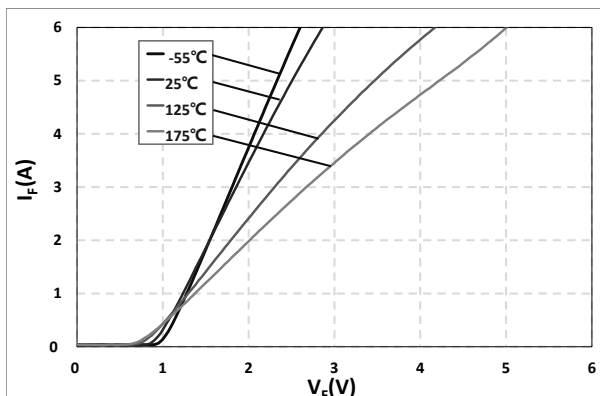


Fig. 1 Typical Forward Characteristics
 $I_F = f(V_F)$; $T_J = -55^\circ\text{C}, 25^\circ\text{C}, 125^\circ\text{C}, 175^\circ\text{C}$

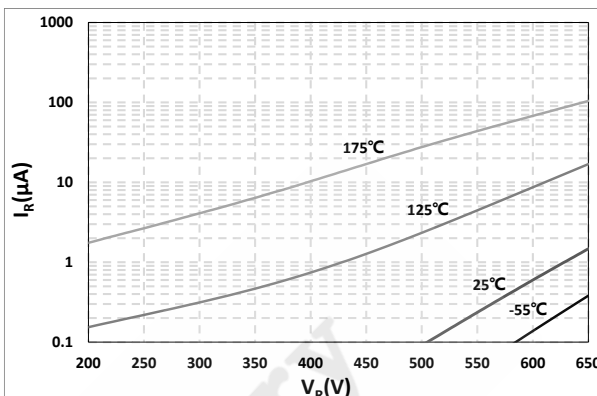


Fig. 2 Reverse Characteristics
 $I_R = f(V_R)$; $T_J = -55^\circ\text{C}, 25^\circ\text{C}, 125^\circ\text{C}, 175^\circ\text{C}$

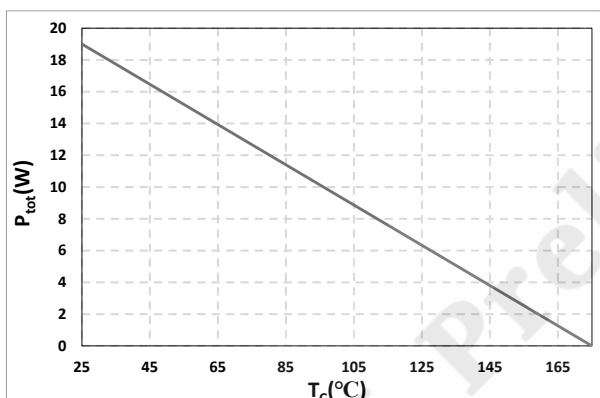


Fig. 3 Typical Power Derating
 $P_{\text{tot}} = f(T_c)$

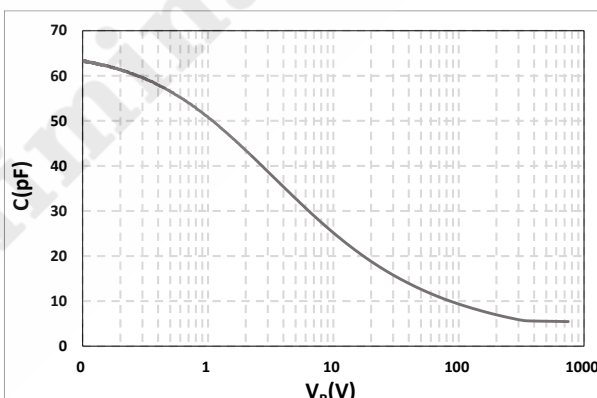


Fig. 4 Typical Total Capacitance
 $C = f(V_R)$

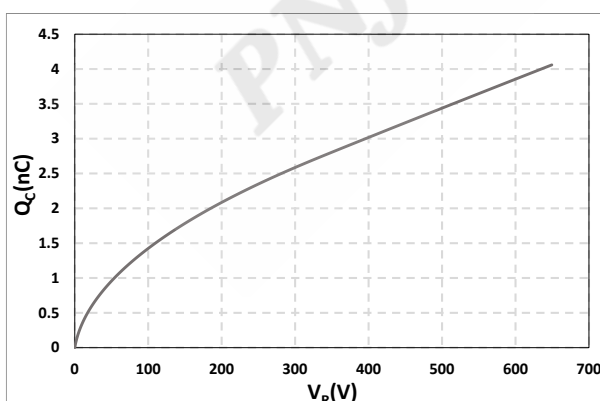


Fig. 5 Typical Total Capacitive Charge
 $Q_C = f(V_R)$

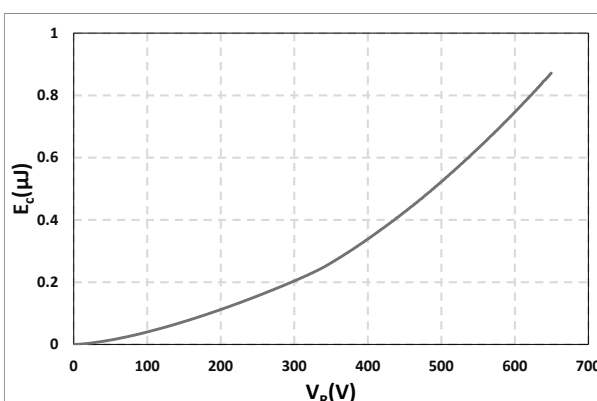
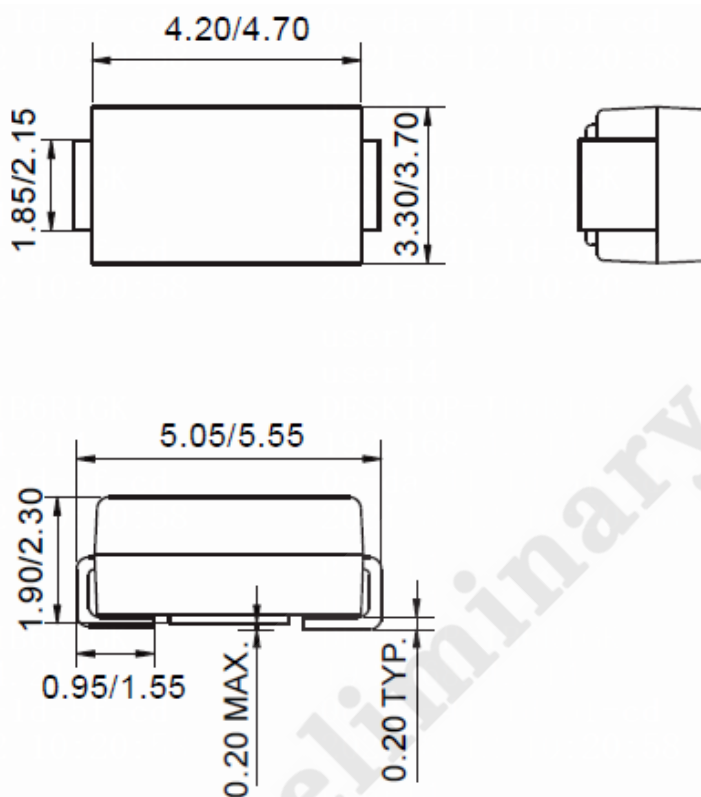


Fig. 6 Capacitance Stored Energy
 $E_C = f(V_R)$

5. Package Outlines



Drawing and Dimensions

PNJ Preliminary